## Appendix 2

```
void main() { char hwswchan;
char unsigned 8 port:
```

5

```
        par {
                parallel_,,port(port);
                SyncGen():
```

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```
                        initialiseRam(port);
                        par {
                                display(hwswchan): sw(hwswchan);
                                y 1 }
                }
```

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RTL description of main